## iC-DP

Rev D1, Page 1/10

## FEATURES

- 36 V highside switch/level shifter
- P-channel output driver without charge pump for short activation time
- Decoupling of input and output reference voltages permits control by 5 V logic
- 200 mA of output current
- Short-circuit protected
- Output with an active freewheeling circuit
- On-chip overtemperature protection with hysteresis
- 4 to 36 V input voltage range
- Input with hysteresis
- Wide temperature range of -40 to $120^{\circ} \mathrm{C}$


## APPLICATIONS

- Highside switch for industrial applications, such as relays, inductive proximity sensors and light barriers


## PACKAGES



SOT23-6L


UDFN6-1.8x2

## BLOCK DIAGRAM



## DESCRIPTION

iC-DP is a monolithic highside switch for ohmic, inductive and capacitive loads.

Designed for a wide input voltage range of 4 to 36 V , it is capable of supplying a minimum output current of 200 mA . The output acts as a current source with a low saturation voltage; protection against short-circuiting is provided by the device shutting down with
excessive temperature. The chip is activated when the input voltage threshold $\mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})$ of typically 3.5 V is exceeded.

When used as a 4-pin element, the input ( $\mathrm{PI}, \mathrm{NI}$ ) and output (DP, VP) reference voltages are decoupled. The maximum permissive voltage difference between VP and PI is 36 V .

## PACKAGING INFORMATION

## PIN CONFIGURATION

 SOT23-6L (JEDEC), 1.6 mm

## PIN FUNCTIONS

## No. Name Function

$1 \mathrm{NI} \quad$ Negative Input
2 PI Positive Input
3 DP Output
4 VP Supply
5 n.c. (not connected)
6 n.c. (not connected)

PIN CONFIGURATION
UDFN6-1.8x2


## PIN FUNCTIONS

## No. Name Function

| 1 | PI | Positive Input |
| :--- | :--- | :--- |
| 2 | VP | Supply |
| 3 | n.c. | (not connected) |
| 4 | DP | Output |
| 5 | n.c | (not connected) |
| 6 NI | Negative Input |  |

PACKAGE DIMENSIONS SOT23-6L


FRONT


RECOMMENDED PCB-FOOTPRINT


All dimensions given in mm. Tolerances of form and position according to JEDEC MO-178.
dra_sot23-61-1_pack_1, 20:1

PACKAGE DIMENSIONS UDFN6-1.8x2


All dimensions given in mm.
Tolerances of form and position according to JEDEC MO-229.

## iC-DP <br> HIGHSIDE SWITCH

Rev D1, Page 5/10

## ABSOLUTE MAXIMUM RATINGS

Beyond these values damage may occur; device operation is not guaranteed. Absolute Maximum Ratings are no Operating Conditions. Integrated circuits with system interfaces, e.g. via cable accessible pins (I/O pins, line drivers) are per principle endangered by injected interferences, which may compromise the function or durability. The robustness of the devices has to be verified by the user during system development with regard to applying standards and ensured where necessary by additional protective circuitry. By the manufacturer suggested protective circuitry is for information only and given without responsibility and has to be verified within the actual system with respect to actual interferences.

| Item No. | Symbol | Parameter | Conditions | Min. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| G001 | V() | VP, PI Input Voltage with reference to NI | V()$=\mathrm{V}(\mathrm{VP})-\mathrm{V}(\mathrm{NI})$ or V()$=\mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})$ | -0.3 | 40 | V |
| G002 | V(DP) | DP Output Voltage with reference to VP | No free wheeling | -40 | 0.3 | V |
| G003 | I(DP) | DP Output Current |  | -300 |  | mA |
| G004 | I(PI) | PI Input Current |  |  | 10 | mA |
| G005 | $\mathrm{I}(\mathrm{NI})$ | NI Input Current |  | -10 |  | mA |
| G006 | Vd() | ESD Susceptibility, all pins with reference to VP, DP | HBM, 100 pF discharged through $1.5 \mathrm{k} \Omega$ |  | 2 | kV |
| G007 | Tj | Max. Junction Temperature |  | -40 | 150 | ${ }^{\circ} \mathrm{C}$ |
| G008 | Ts | Storage Temperature Range |  | -40 | 150 | ${ }^{\circ} \mathrm{C}$ |
| G009 | Eas | Inductive load switch-off energy dissipation | Temperature monitor not active, Tj < Ton |  | 5 | mJ |

## THERMAL DATA

Operating Conditions: $\mathrm{V}(\mathrm{PI})=4 \ldots 36 \mathrm{~V}$, unless otherwise stated

| Item <br> No. | Symbol | Parameter | Conditions | Unit |
| :--- | :--- | :--- | :---: | :---: |
| T01 | Ta | Ambient Temperature Range |  | Min. | Typ. | Max. |
| :---: |

Rev D1, Page 6/10

## ELECTRICAL CHARACTERISTICS

Operating Conditions: $\mathrm{V}(\mathrm{PI})=0 \ldots 36 \mathrm{~V}, \mathrm{Tj}=-40 \ldots 120^{\circ} \mathrm{C}$, unless otherwise stated

| Item No. | Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Total Device |  |  |  |  |  |  |  |
| 001 | V() | VP, PI Supply Voltage | V()$=\mathrm{V}(\mathrm{VP})-\mathrm{V}(\mathrm{NI})$ bzw. V()$=\mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})$ | 4 |  | 36 | V |
| 002 | I(PI) | PI Supply Current | No load; $\begin{aligned} & \mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})>\mathrm{V}(\mathrm{PI}) \text { on } \\ & \mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})<\mathrm{V}(\mathrm{PI}) \text { off } \end{aligned}$ | $\begin{aligned} & 0 \\ & 0 \end{aligned}$ |  | $\begin{gathered} 1000 \\ 250 \end{gathered}$ | $\mu \mathrm{A}$ $\mu \mathrm{A}$ |
| 003 | I(VP) | VP Supply Current | No load; $\begin{aligned} & \mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})>\mathrm{V}(\mathrm{PI}) \text { on } \\ & \mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})<\mathrm{V}(\mathrm{PI}) \text { off } \end{aligned}$ | $\begin{gathered} 80 \\ 0 \end{gathered}$ |  | $\begin{gathered} 680 \\ 2000 \end{gathered}$ | $\mu \mathrm{A}$ $\mu \mathrm{A}$ |
| 004 | I(NI) | NI Input Current | No load; $\begin{aligned} & \mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})>\mathrm{V}(\mathrm{PI}) \text { on } \\ & \mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})<\mathrm{V}(\mathrm{PI}) \text { off } \end{aligned}$ | $\begin{aligned} & -1000 \\ & -2000 \end{aligned}$ |  | $\begin{gathered} -130 \\ 0 \end{gathered}$ | $\mu \mathrm{A}$ $\mu \mathrm{A}$ |
| 005 | Ilk(DP) | DP Output Leakage Current | $\begin{aligned} & \mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})<\mathrm{V}(\mathrm{PI}) \text { off, } \\ & \mathrm{V}(\mathrm{DP})=0 . . \mathrm{V}(\mathrm{VP}) \end{aligned}$ | -100 |  | 100 | $\mu \mathrm{A}$ |
| 006 | Vc(DP)lo | DP Clamp Voltage low | $\begin{aligned} & \mathrm{Vc}(\mathrm{DP}) \mathrm{lo}=\mathrm{V}(\mathrm{DP})-\mathrm{V}(\mathrm{VP}), \\ & \mathrm{l}(\mathrm{DP})=-10 \mathrm{~mA} \end{aligned}$ | -70 | -45 | -40 | V |
| 007 | $\mathrm{Vc}(\mathrm{DP}) \mathrm{hi}$ | DP Clamp Voltage high | $\begin{aligned} & \mathrm{Vc}(\mathrm{DP}) \mathrm{hi}=\mathrm{V}(\mathrm{DP})-\mathrm{V}(\mathrm{VP}), \\ & \mathrm{I}(\mathrm{DP})=10 \mathrm{~mA} \end{aligned}$ | 0.3 |  | 1 | V |
| 008 | Vc() hi | PI, VP Clamp Voltage high | Vc() $\mathrm{hi}=\mathrm{V}()-\mathrm{V}(\mathrm{NI}), \mathrm{I}()=4 \mathrm{~mA}$ | 37 | 40 |  | V |
| 009 | tpiohi | Activation Delay NI $\rightarrow$ DP | $\begin{aligned} & \mathrm{V}(\mathrm{PI}) \mathrm{on}<\mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})<48 \mathrm{~V}, \\ & \mathrm{~V}(\mathrm{Rload})=48 \mathrm{~V}, \mathrm{Rload}=360 \Omega, \\ & \mathrm{I}(\mathrm{DP})=0 \rightarrow-90 \mathrm{~mA}, \\ & \text { \|Input slew rate\| }>10 \mathrm{~V} / \mu \mathrm{s} \end{aligned}$ | 1 |  | 25 | $\mu \mathrm{s}$ |
| 010 | tpiolo | Deactivation Delay NI $\rightarrow$ DP | $\begin{aligned} & \mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})<\mathrm{V}(\mathrm{PI}) \text { off, } \\ & \mathrm{V}(\mathrm{Rload})=36 \mathrm{~V}, \mathrm{Rload}=360 \Omega, \\ & \mathrm{I}(\mathrm{DP})=-100 \rightarrow-10 \mathrm{~mA}, \\ & \mid \text { Input slew rate\| }>10 \mathrm{~V} / \mathrm{ss} \end{aligned}$ | 1 |  | 15 | $\mu \mathrm{s}$ |
| Highside Output DP |  |  |  |  |  |  |  |
| 101 | Vs(DP) | Output Saturation Voltage | $\begin{aligned} & D P=\text { hi, with reference to } \mathrm{VP} \\ & \mathrm{I}(\mathrm{DP})=-200 \mathrm{~mA}, \\ & \mathrm{I}(\mathrm{DP})=-50 \mathrm{~mA} \end{aligned}$ | $\begin{aligned} & -800 \\ & -200 \end{aligned}$ |  |  | $\begin{aligned} & \mathrm{mV} \\ & \mathrm{mV} \end{aligned}$ |
| 102 | Isc(DP) | Output Short-Circuit Current | $\begin{aligned} & \mathrm{V}(\mathrm{VP})-\mathrm{V}(\mathrm{DP})=1 \mathrm{~V} \ldots \mathrm{VB}, \mathrm{DP}=\mathrm{hi} \\ & \mathrm{Tj}=-40^{\circ} \mathrm{C} \\ & \mathrm{Tj}=27^{\circ} \mathrm{C} \\ & \mathrm{Tj}=120^{\circ} \mathrm{C} \end{aligned}$ | -800 | -400 | $\begin{aligned} & -200 \\ & -200 \\ & -200 \end{aligned}$ | mA <br> mA <br> mA |
| 103 | SR(DP)on | Slew Rate, V(DP) $\rightarrow$ VP | $\begin{aligned} & \mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})>\mathrm{V}(\mathrm{PI}) \text { on, } \\ & \mathrm{V}(\mathrm{Rload})=36 \mathrm{~V}, \mathrm{Rload}=360 \Omega, \\ & \mathrm{~V}(\mathrm{VP})-\mathrm{V}(\mathrm{DP})=32.4 \rightarrow 3.6 \mathrm{~V} \end{aligned}$ |  | 50 |  | $\mathrm{V} / \mu \mathrm{s}$ |
| 104 | SR(DP)off | Slew Rate, V(DP) $\rightarrow$ V(NI) | $\begin{aligned} & \mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})<\mathrm{V}(\mathrm{PI}) \text { off, } \\ & \mathrm{V}(\mathrm{Rload})=36 \mathrm{~V}, \mathrm{Rload}=360 \Omega, \\ & \mathrm{~V}(\mathrm{VP})-\mathrm{V}(\mathrm{DP})=3.6 \rightarrow 32.4 \mathrm{~V} \end{aligned}$ |  | 20 |  | $\mathrm{V} / \mu \mathrm{s}$ |
| 105 | Vfw(DP) | Freewheeling Voltage | $I(D P)=-200 \mathrm{~mA},$ <br> with reference to VP, including Zener noise voltage | -60 | -45 | -40 | V |
| Temperature Monitor |  |  |  |  |  |  |  |
| 201 | Toff | Thermal Shutdown Threshold |  | 120 |  | 150 | ${ }^{\circ} \mathrm{C}$ |
| 202 | Ton | Thermal Release Threshold | Decreasing temperature | 110 |  | 135 | ${ }^{\circ} \mathrm{C}$ |
| 203 | Thys | Thermal Shutdown Hysteresis | Thys = Toff - Ton |  | 15 |  | ${ }^{\circ} \mathrm{C}$ |
| Input Threshold |  |  |  |  |  |  |  |
| 301 | $\mathrm{V}(\mathrm{PI})$ on | Power-On Threshold Voltage | $\mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})$ | 2.7 |  | 4.1 | V |
| 302 | $\mathrm{V}(\mathrm{PI})$ off | Power-Off Threshold Voltage | $\mathrm{V}(\mathrm{PI})-\mathrm{V}(\mathrm{NI})$, decreasing voltage | 2.3 |  | 3.7 | V |
| 303 | V(PI)hys | Hysteresis | $\mathrm{V}(\mathrm{PI})$ hys $=\mathrm{V}(\mathrm{PI})$ on $-\mathrm{V}(\mathrm{PI})$ off | 170 | 380 | 590 | mV |

## ELECTRICAL CHARACTERISTIC: DIAGRAMS

## Simulation Data

(current consumption without load; leakage currents not included)


Figure 1: NI input current, no load


Figure 3: NI input current, $\mathrm{I}(\mathrm{DP})=-100 \mathrm{~mA}$


Figure 5: VP supply current, no load


Figure 7: DP output characteristic


Figure 2: NI input current, $\mathrm{I}(\mathrm{DP})=-5 \mathrm{~mA}$


Figure 4: PI input current, load independent


Figure 6: DP short-circuit output current

## APPLICATION NOTES



Figure 8: 36 V supply, NPN input control


Figure 10: $5 \mathrm{~V} \mu \mathrm{C}$ operating at 5 to 0 V input control, 36 V output supply


Figure 9: 36 V supply, PNP input control


Figure 11: 12 V NPN input control, 36 V output supply

Rev D1, Page 9/10

## REVISION HISTORY

| Rel. | Rel. Date* | Chapter | Modification | Page |
| :--- | :--- | :--- | :--- | :--- |
| C3 | $2011-06-29$ |  | previous version |  |


| Rel. | Rel. Date ${ }^{*}$ | Chapter | Modification | Page |
| :--- | :--- | :--- | :--- | :--- |
| D1 | $2016-04-07$ | PACKAGES | package type UDFN6 introduced | $2,3,4$ |
|  | $2016-04-07$ | ORDERING INFORMATION | ordering information added for UDFN6 package | 10 |
|  | $2016-04-07$ | APPLICATION NOTES | figures modified | 8 |

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* Release Date format: $Y Y Y Y-M M-D D$

HIGHSIDE SWITCH

## ORDERING INFORMATION

| Type | Package | Order Designation |
| :--- | :--- | :--- |
| iC-DP | SOT23-6L <br> UDFN6-1.8x2 | iC-DP SOT23-6L <br> iC-DP UDFN6-1.8x2 |

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